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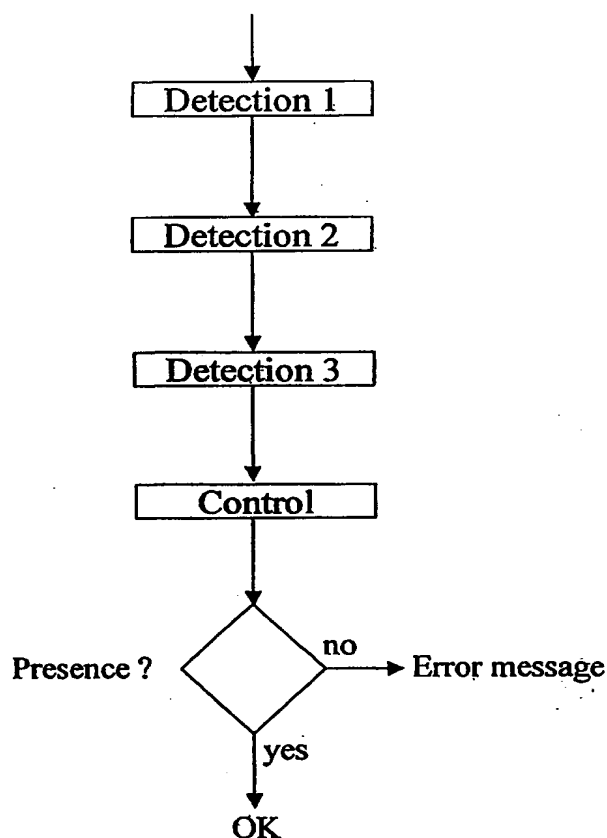
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(54) Title: PROCESS AND APPARATUS TO CONTROL THE INTEGRITY OF A PLANAR SUBSTRATE



(57) Abstract: The process comprises the following steps: detection of the passage of an edge of the substrate by a first trigger; detection of the passage of said edge of the substrate at least at a first selected checkpoint on the substrate; control of the presence of the detection of the edge of the substrate at said at least first checkpoint between said detection by said triggers and generation of an integrity check failed message in the absence of the detection of the edge of the substrate at said checkpoint.

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# INTERNATIONAL SEARCH REPORT

International Application No  
PCT/IB 03/05790

## A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 B65H7/06

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 B65H

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the International search (name of data base and, where practical, search terms used)

PAJ, EPO-Internal

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 443 257 A (TOSHIBA) 22 August 1995 (1995-08-22) the whole document	1-11
A	PATENT ABSTRACTS OF JAPAN vol. 009, no. 084 (M-371), 13 April 1985 (1985-04-13) & JP 59 212345 A (FUJITSU KK), 1 December 1984 (1984-12-01) abstract	1,2,5,6, 9
A	EP 0 317 655 A (KOMORI) 31 May 1989 (1989-05-31) the whole document	1
A	US 4 623 975 A (TOKYO SHIBAURA ELECTRIC) 18 November 1986 (1986-11-18) the whole document	1

☐ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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